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## ЦИФРОВЫЕ ПЛАТЫ

52401-25-200M;  
52405-5-3, 10-2, 25-1, 25-3;  
52401E-6-1, 25-200M;  
52405E-5-3, 10-2, 25-1, 25-3;  
33010, 36010, 36020

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## ТЕХНИЧЕСКИЕ ХАРАКТЕРИСТИКИ

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## MODEL 52400 SERIES

### KEY FEATURES

- Hybrid compatible PXI
- Four quadrant operation
- 18-bit source/measure resolution (multiple selectable ranges)
- Low output noise
- High measurement speed (100k s/S)
- High output slew rate
- Optional measurement log
- DIO/Trigger bits
- Output profiling by hardware sequencer
- Programmable output resistance
- Floating & Guarding output
- 16 Control Bandwidth Selection
- Master / Slave operation
- Driver with LabView/LabWindows & C/C# API
- Softpanel GUI

### APPLICATIONS

- Semiconductor test
- LED / laser diode test
- Battery test
- Transistor test
- Solar cell test
- Electric vehicle test
- Avionics test
- Power electronics test
- Sensor test

## HIGH PRECISION SOURCE MEASURE UNIT MODEL 52400 SERIES

The 52400 series is a PXI based SMU (Source Measurement Unit) card designed for highly accurate source or load simulation with precision voltage and current measurements.

The SMU combines four-quadrant operation with precision and high speed measurement. This makes the SMU an ideal instrument in many parametric test applications ranging from ICs, two-lead components such as sensors, LEDs, laser diodes, transistors, to solar cells, batteries and many other electronic devices.

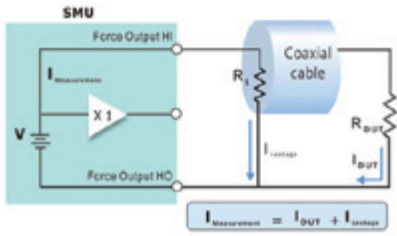
The 52400 series features: 16 selectable control bandwidths to ensure high speed output and stable operation; multiple source/measure ranges with an 18-bit DAC/ADC to provide the best resolution and accuracy available with a sampling rate up to 100K s/S; programmable internal series resistance for battery simulation;  $\pm$ force,  $\pm$ sense and  $\pm$ guards lines to avoid leakage current and reduce settling time -- especially useful for low current test applications.

The 52400 series has a patented hardware sequence engine that uses deterministic timing to control each SMU. The sequencer's on-board memory can store up to 65535 sequencer commands and 32k measurement samples per channel, allowing cross module/ card synchronization and latency free output control and measurement. No PC communication is required during execution of the hardware sequencer test process.

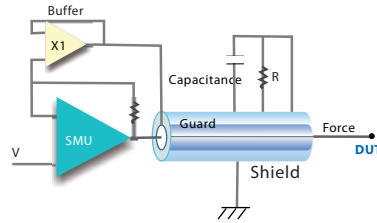
C, C#, LabView, LabWindows APIs and versatile soft front panels come standard with each SMU. The back connectors are compatible with both PXIe and hybrid chassis. All of these features enable easy integration to PXI or PXI-hybrid systems designed for a wide range of applications.



## GUARDING FOR LOW CURRENT APPLICATION



Leakage current flows through the cable's insulation resistance



Guard Connection: Cable Capacitance is eliminated with Triaxial Cable

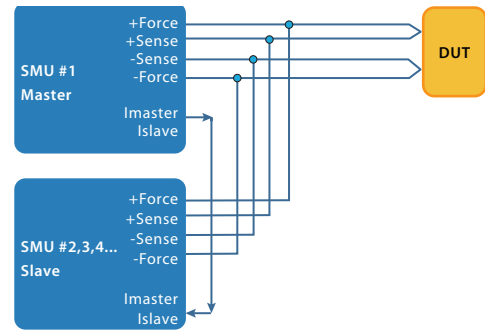
Guarding is an important technique for very-low current measurements. Guarding reduces leakage current error and decreases settling time. This is achieved by keeping the potential of the guard conductor at the same potential as the force conductor, so current does not flow between the force and guard conductors. Guarding also eliminates the cable capacitance between the SMU and DUT.

The 52400 series features two  $\pm$  guard wires per channel, resulting in faster and more accurate measurements.

## MASTER/SLAVE OPERATION

For maximum flexibility, the 52405 SMUs support Master/Slave operation when higher current under FVMI (Force Voltage Measure Current) mode is required. To ensure accurate current sharing between modules and maximum performance, Master/Slave operation is only allowed between SMUs of the same model number.

Current sharing is achieved by one channel operating as the Master under FVMI mode while the Slaves operate in FIMV mode. The Master channel is programmed in voltage mode while the Slaves are set to current mode. The Slaves will follow the Master's set voltage. The wiring diagram for current sharing in master/slave control is shown to the right.



Wiring Structure for Master/Slave Control

## SPECIFICATIONS

Model Name	52401-25-200m	52405-5-3 <sup>*1</sup>	52405-10-2 <sup>*1</sup>	52405-25-1 <sup>*1</sup>	52405-25-3 <sup>*1</sup>
Slots			1		
Output Channels			2		
Source	5W x 2		25W x 2		
Load	5W x 2		10W x 2		
Input Voltage	External 48VDC source required <sup>*2</sup>				
Input Current	0.7A Max		2.2A Max		
Output Isolation	Isolated		Isolated by External Power Supply		
Bit Resolution	18 bits				
Programmable Loop Bandwidth	16 steps				
Force Voltage Ranges	$\pm 25V, \pm 10V, \pm 5V, \pm 2.5V, \pm 1V, \pm 500mV$	$\pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$
Force Current Ranges	$\pm 200mA, \pm 20mA, \pm 2mA, \pm 200\mu A, \pm 20\mu A, \pm 2\mu A, \pm 200nA$	$\pm 3.5A, \pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 3.5A (\leq 5V), \pm 2.5A (\leq 10V), \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$
Measure Voltage Ranges	$\pm 25V, \pm 10V, \pm 5V, \pm 2.5V, \pm 1V, \pm 500mV, \pm 250mV, \pm 100mV, \pm 50mV, \pm 25mV, \pm 10mV, \pm 4mV$	$\pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$
Measure Current Ranges	$\pm 200mA, \pm 20mA, \pm 2mA, \pm 200\mu A, \pm 20\mu A, \pm 2\mu A, \pm 200nA$	$\pm 3.5A, \pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 3.5A (\leq 5V), \pm 2.5A (\leq 10V), \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$

## SPECIFICATIONS

Model Name	52401-25-200m	52405-5-3 <sup>**1</sup>	52405-10-2 <sup>**1</sup>	52405-25-1 <sup>**1</sup>	52405-25-3 <sup>**1</sup>
Force Voltage Accuracy	0.05% reading + 0.0076% F.S. ( $\geq 500\text{mV}$ Range) 0.02% reading + 25 $\mu\text{V}$ ( $<500\text{mV}$ Range)	0.05% reading + 0.008% F.S. ( $\geq 500\text{mV}$ Range) 0.05% reading + 25 $\mu\text{V}$ ( $<500\text{mV}$ Range)			
Force Current Accuracy	0.05% reading + 0.05% F.S. ( $\geq 2\mu\text{A}$ Range) 0.05% reading + 200pA ( $<2\mu\text{A}$ Range)	0.1% reading + 0.1% F.S. ( $>1\text{A}$ Range) 0.05% reading + 0.05% F.S. ( $\leq 1\text{A}$ Range)			
Measure Voltage Accuracy	0.05% reading + 0.0076% F.S. ( $\geq 500\text{mV}$ Range) 0.05% reading + 25 $\mu\text{V}$ ( $<500\text{mV}$ Range)	0.05% reading + 0.008% F.S. ( $\geq 500\text{mV}$ Range) 0.05% reading + 25 $\mu\text{V}$ ( $<500\text{mV}$ Range)			
Measure Current Accuracy	0.05% reading + 0.05% F.S. ( $\geq 2\mu\text{A}$ Range) 0.05% reading + 200pA ( $<2\mu\text{A}$ Range)	0.1% reading + 0.12% F.S. ( $>1\text{A}$ Range) 0.05% reading + 0.05% F.S. ( $\leq 1\text{A}$ Range)			
Wideband Source Noise	< 20 mV pp 20Mhz BW No Load				
Measurement Sampling Rate	100K Samples/s				
Output Connection	6 Wires ( $\pm$ Force, $\pm$ Sense, $\pm$ Guard)				
Measurement Log	32K Samples/channel				
Output Profiling	65535 Steps				
Trigger Input	1 Ch	Programmable 8 Ch			
Trigger Output					
Floating Output	Channel Isolated				
Master/Slave Mode	No	Yes			
Programmable Resistance	No	Yes			
Regulatory Compliance	CE/FCC				

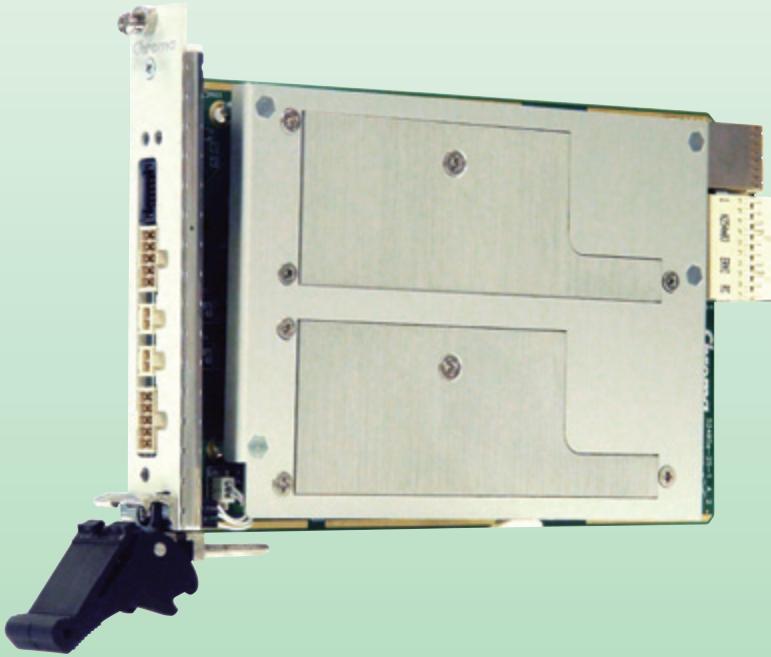
**Note \*1** : The limitation of the duty cycle for 52405 series.

Below are the maximum duty cycles while PXI-SMU card is at full load with frequency larger than 1Hz :

duty cycle = 50% at 2.5A range ; duty cycle = 40% at 3.5A range

When the PXI-SMU card is over temperature, it will automatically disconnect output to protect the unit

**Note \*2** : Required voltage range 48V  $\pm$  5% ; required voltage noise  $\leq 100\text{mVpp}$



## HIGH PRECISION SOURCE MEASURE UNIT MODEL 52400e SERIES

The 52400e series is a PXIe based SMU (Source Measurement Unit) card designed for highly accurate source or load simulation with precision voltage and current measurements.

The SMU combines four-quadrant operation with precision and high speed measurement. This makes the SMU an ideal instrument in many parametric test applications ranging from ICs, two-lead components such as sensors, LEDs, laser diodes, transistors, to solar cells, batteries and many other electronic devices.

The 52400e series features: 16 selectable control bandwidths to ensure high speed output and stable operation; multiple source/measure ranges with an 18-bit DAC/ADC to provide the best resolution and accuracy available with a sampling rate up to 100K s/s; programmable internal series resistance for battery simulation;  $\pm$ force,  $\pm$ sense and  $\pm$ guards lines to avoid leakage current and reduce settling time -- especially useful for low current test applications.

The 52400e series has a patented hardware sequence engine that uses deterministic timing to control each SMU. The sequencer's on-board memory can store up to 65535 sequencer commands and 32k measurement samples per channel, allowing cross module/ card synchronization and latency free output control and measurement. No PC communication is required during execution of the hardware sequencer test process.

C, C#, LabView, LabWindows APIs and versatile soft front panels come standard with each SMU. The back connectors are compatible with both PXIe and hybrid chassis. All of these features enable easy integration to PXIe or PXI-hybrid systems designed for a wide range of applications.

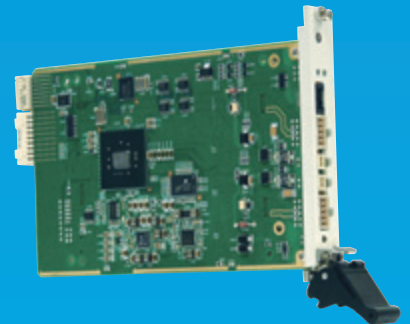
## MODEL 52400e SERIES

### KEY FEATURES

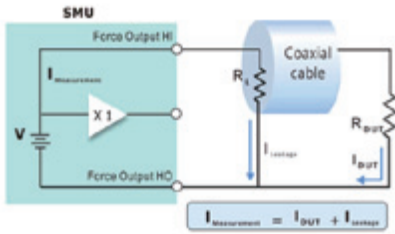
- 1-Slot PXI express module
- 2/4 independent channels (4 ch for Model 52401e-6-1)
- 18-bit resolution
- Four quadrant operation
- 6-wire Force/Sense/Guard
- Low output noise
- DIO/Trigger bits
- Deterministic hardware sequencer
- Programmable resistance
- 16 control bandwidth selection
- Master/Slave operation
- LabView/LabWindows & C/C#
- Softpanel GUI

### APPLICATIONS

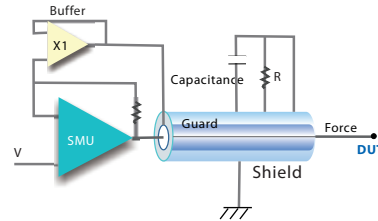
- Semiconductor
- LED / Laser Diode
- Solar Cell
- Battery / BMS
- Transistor
- Automotive
- Avionics
- Power Electronics
- Sensor / IoT



## GUARDING FOR LOW CURRENT APPLICATION



Leakage current flows through the cable's insulation resistance



Guard Connection: Cable Capacitance is eliminated with Triaxial Cable

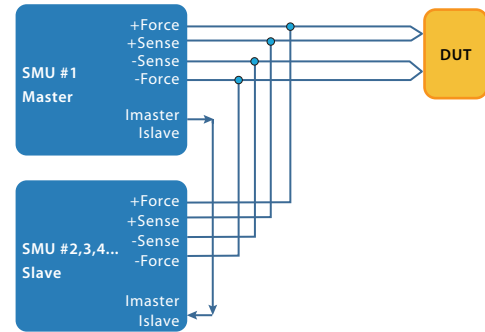
Guarding is an important technique for very-low current measurements. Guarding reduces leakage current error and decreases settling time. This is achieved by keeping the potential of the guard connector at the same potential as the force conductor, so current does not flow between the force and guard conductors. Guarding also eliminates the cable capacitance between the SMU and DUT.

The 52400e series features two  $\pm$ guard wires per channel, resulting in faster and more accurate measurements.

## MASTER/SLAVE OPERATION

For maximum flexibility, the 52405e SMUs support Master/Slave operation when higher current under FVMI (Force Voltage Measure Current) mode is required. To ensure accurate current sharing between modules and maximum performance, Master/Slave operation is only allowed between SMUs of the same model number.

Current sharing is achieved by one channel operating as the Master under FVMI mode while the Slaves operate in FIMV mode. The Master channel is programmed in voltage mode while the Slaves are set to current mode. The Slaves will follow the Master's set voltage. The wiring diagram for current sharing in master/slave control is shown to the right.



Wiring Structure for Master/Slave Control

## SPECIFICATIONS

Model Name	52401e-6-1	52401e-25-200m	52405e-5-3 *1	52405e-10-2 *1	52405e-25-1 *1	52405e-25-3 *1
Slots						1
Output Channels	4				2	
Source	3W x 4	5W x 2				25W x 2
Load	1.8W x 4	5W x 2				10W x 2
Input Voltage	Backplane Power		External 48VDC source required *2			
Input Current	2.5A Max	0.7A Max	2.2A Max			
Output Isolation	Isolated but share common LO	Isolated	Isolated by External Power Supply			
Bit Resolution	16 Bits	18 bits				
Programmable Loop Bandwidth	8 steps	16 steps				
Settling Time	<30 $\mu$ Sec, typically					
Force Voltage Ranges	$\pm 6V$	$\pm 25V, \pm 10V, \pm 5V, \pm 2.5V, \pm 1V, \pm 500mV$	$\pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$
Force Current Ranges	$\pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A$	$\pm 200mA, \pm 20mA, \pm 2mA, \pm 200\mu A, \pm 20\mu A, \pm 2\mu A, \pm 200nA$	$\pm 3.5A, \pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 3.5A(\leq 5V), \pm 2.5A(\leq 10V), \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$
Measure Voltage Ranges	$\pm 6V$	$\pm 25V, \pm 10V, \pm 5V, \pm 2.5V, \pm 1V, \pm 500mV, \pm 250mV, \pm 100mV, \pm 50mV, \pm 25mV, \pm 10mV, \pm 4mV$	$\pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$	$\pm 25V, \pm 12.5V, \pm 10V, \pm 5V, \pm 2V, \pm 1V, \pm 500mV, \pm 200mV, \pm 100mV$
Measure Current Ranges	$\pm 1A, \pm 100mA, \pm 10mA, \pm 100\mu A, \pm 10\mu A$	$\pm 200mA, \pm 20mA, \pm 2mA, \pm 200\mu A, \pm 20\mu A, \pm 2\mu A, \pm 200nA$	$\pm 3.5A, \pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 2.5A, \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$	$\pm 3.5A(\leq 5V), \pm 2.5A(\leq 10V), \pm 1A, \pm 100mA, \pm 10mA, \pm 1mA, \pm 100\mu A, \pm 10\mu A, \pm 1\mu A$

## SPECIFICATIONS

Model Name	52401e-6-1	52401e-25-200m	52405e-5-3 *1	52405e-10-2 *1	52405e-25-1 *1	52405e-25-3 *1
Force Voltage Accuracy	0.02% reading + 0.01% F.S.	0.05% reading + 0.0076% F.S. (≥500mV Range) 0.02% reading + 25uV (<500mV Range)	0.05% reading + 0.008% F.S. (≥500mV Range) 0.05% reading + 25uV (<500mV Range)			
Force Current Accuracy	0.1% reading + 0.1% F.S. (1A Range) 0.05% reading + 0.05% F.S. (<1A Range)	0.05% reading + 0.05% F.S. (≥2uA Range) 0.05% reading + 200pA (<2uA Range)	0.1% reading + 0.1% F.S. (>1A Range) 0.05% reading + 0.05% F.S. (≤1A Range)			
Measure Voltage Accuracy	0.02% reading + 0.01% F.S.	0.05% reading + 0.0076% F.S. (≥500mV Range) 0.05% reading + 25uV (<500mV Range)	0.05% reading + 0.008% F.S. (≥500mV Range) 0.05% reading + 25uV (<500mV Range)			
Measure Current Accuracy	0.1% reading + 0.1% F.S. (1A Range) 0.05% reading + 0.05% F.S. (<1A Range)	0.05% reading + 0.05% F.S. (≥2uA Range) 0.05% reading + 200pA (<2uA Range)	0.1% reading + 0.12% F.S. (>1A Range) 0.05% reading + 0.05% F.S. (≤1A Range)			
Wideband Source Noise	< 30 mV pp 20Mhz BW No Load					
Measurement Sampling Rate	600K Samples/s	100K Samples/s				
Output Connection	5 Wires (± Force, ± Sense, +Guard)	6 Wires (± Force, ± Sense, ± Guard)				
Measurement Log	32K Samples/channel					
Output Profiling	65535 Steps					
Trigger Input	Programmable 4 Ch	1 Ch	Programmable 8 Ch			
Trigger Output						
Floating Output	No	Channel Isolated				
Master/Slave Mode	Yes	No	Yes			
Programmable Resistance	Yes	No	Yes			
Regulatory Compliance	CE/FCC					

**Note \*1** : If chassis has less than 38.2W/slot, then the below output limitations apply.

2.5Amp range = 50% on duty cycle, 500mSec maximum continuous on time

3.5Amp range = 40% on duty cycle, 500mSec maximum continuous on time (1250mSec off during maximum on time case)

If the PXI-SMU card is over temperature, it will automatically disconnect output to protect the unit.

**Note \*2** : Required Voltage Range 48V ± 5% ; Required Voltage Noise ≤ 100mVpp

All specifications are subject to change without notice.

## PROGRAMMABLE HIGH SPEED PXIE DIGITAL IO CARD MODEL 33010

33010 is a high-density 100MHz PXIe digital IO card designed for characterizing, validating, and testing a variety of digital and mixed-signal ICs. Each IO card consists of a Sequencer Pattern Generator (SQPG) and 32 channels of full ATE-like features. The 33010 IO card is expandable up to 256 channels. Some unique features of the 33010 include an on-board SQPG, per pin timing/levels/PMU/TFMU, multiple time domains, and multi-threaded testing for complex IC testing. Each channel is also equipped with 64M vector memory, 16 timing sets with on-the-fly timing change, and per pin timing and frequency measurements up to 400 MHz.

### Proprietary Software, CRAFT\_PXI and other rich features of software support

In addition to LabView and LabWindows support, provides a proprietary software option, CRAFT\_PXI, for Windows-based systems. CRAFT\_PXI contains a full set of production tools and user debugging tools. The production tools include ease-of-use GUI

software with an Operator Interface, Test Data Output, Binning and Sequence Control, Wafer Map, Summary Tool, and rich sets of prober/handler drivers. The user debugging tools include a Data Logger, Debug Plan, TCM, Shmoo, Pattern Editor, Waveform, and more. A CAD to ATE pattern conversion tool is also supported to cover WGL/STIL/VCD/EVCD conversions.

### Addressing the emerging market and test cost challenges

With a high-density per pin and per site architecture, full suite of ATE Pin Electronics (PE) card functions, expandable channel count, and a rich set of software support, the 33010 digital IO card will help users address the emerging market and test cost challenges. 33010 PXIe cards can be easily adopted with other PXI/PXIe solutions such as RF, SMUs, and Mixed-signal cards to address a variety of applications such as MCUs, Sensors, RF ICs, PMICs, or ICs with combined functions.

## MODEL 33010

### KEY FEATURES

- Standard PXIe-Hybrid [3U] compatible bus type
- 100MHz maximum clock rate
- 32 channels per board
- Extendable up to 256 channels in one chassis
- Any pin to any site
- Per board sequencer architecture (multiple time domains supported)
- Per-pin timing with per-pin, per-cycle bidirectional control
- Per-pin frequency measurement unit
- Per-pin DC level & PMU
- 16 timing sets with on-the-fly timing changes
- 64M sequencer command memory per pin
- 64M vector memory per pin
- SCAN pattern function support
- Windows 7 operating system
- LabView and LabWindows support
- Proprietary CRAFT\_PXI software tools option
- Master / Slave architecture for boards chaining
- Similar to pattern and timing structure as 3380D/3380P/3380 series ATE

### APPLICATIONS

- Semiconductor
- LED / Laser Diode
- Solar Cell
- Battery / BMS
- Transistor
- Automotive
- Avionics
- Power Electronics
- Sensor / IoT





## SPECIFICATIONS

<b>Model</b>	<b>33010</b>
Clock Rate	100 Mhz
Pin Channels per Card	32 pins (chained to max. 256 pins)
Pattern Memory	64M
Sequence Control Memory	64M
Parallel Testing Capability	Any pin to any site
<b>Timing Generator per Pin</b>	
Timing Generators	8 edges per pin (4 drive / 2 strobes / 2 IO markers)
No. of Timing Sets	16
Rate Setting Resolution	625 pS
Rate Setting Range	10ns to 5ms
<b>Driver / Comparator / Load</b>	
Pin Driver (Vil/Vih) Range	-1.5V to +6V
Pin Driver(Vil/Vih) Accuracy	± 10mV
Output Current Limit	75 mA
Output Impedance	50 ± 5 Ω
Pin Comparator (Voh/Vol) Range	-1.5V to +6V
Pin Comparator (Voh/Vol) Accuracy	± 10mV
Pin Load (Iol/Ioh) Range	± 25mA
Vref Setting Range	-1.5V to +6V
<b>Scan Chains</b>	
Scan Chains Numbers	Configurable to 1, 2, 4, 8 chains per board
Scan Pattern Memory Size	3G / 1.5G / 768M / 384M
<b>PPMU</b>	
Channel	Per Pin (32 Chs FIMV / FVMI)
Voltage Force Range	-2.0V to +6V
Current Measured Range	± 2uA / ± 10uA / ± 100uA / ± 1mA / ± 40mA
Current Force Range	± 2uA / ± 10uA / ± 100uA / ± 1mA / ± 40mA
Voltage Measured Range	-2.0V to +6V
<b>Time &amp; Frequency Measurement Unit</b>	
Maximum Frequency Measurement	Per pin, 400MHz
Maximum Time Measurement	Per pin, 40 sec. (0.025Hz / resolution : 10ns)
Free-run Clock	Per Pin, Max. : 200MHz
<b>Others</b>	
System Environment	Window 7
Programming Language	C \ C# \ Labview
Power Consumption	80W
Dimension	PXle 3U

<b>Optional PXIe Power Supply</b>	<b>A330101 (AP15)</b>
Input Voltage (VAC)	100 ~ 240 ± 10% V <sub>LN</sub>
Source Line Frequency Range	47 ~ 63Hz
Input Current , Continuous (A)	0.1 ~ 2.7A
Output Range (Vdc)	17.6~18.9VDC ± 5%
Output Current, Continuous (A)	11.2A
Output Voltage Ripple Noise	150mV
Max. Support Watt	up to 200W (33010 x 4)
Occupy Slots	2 slots

## MODEL 36010

### KEY FEATURES

- Standard PXI 3U bus type
- 100MHz maximum data rate
- 8 channels with per-pin, per-cycle bidirectional control
- Scalable architecture to provide up to 64-pin
- 32M sequence command memory
- More than 17 pattern sequence commands
- Per-pin architecture
- 32M vector memory per pin
- 32 sets of clock and waveform per pin
- Waveforms changes on-the-fly
- Programmable tri-level driver in 610uV resolution
- One high voltage driver per board
- Per-channel PMU
- Per-channel timing measurement unit
- Support scan pattern function
- Windows XP/7 operating system
- Support LabView and LabWindows
- Proprietary software tools option

### APPLICATIONS

- Logic and mixed signal validation and test
- Digital pattern generator and vector capture
- Consumer IC and electronics test
- Logic test subsystem for DC and RF ATE

## PROGRAMMABLE PIN ELECTRONICS MODULE

### MODEL 36010

The 36010 is a 100MHz programmable pin electronic module designed for characterizing, validating and testing digital and mixed signal IC or electronics. Each module consists of a Sequence Pattern Generator and Logic Pin Electronics Card containing 8 channels. The 36010 module is expandable to provide up to 64 channels hardware resource for various purposes. Besides, based on the per-pin architecture, each channel is equipped with 32M vector memory, 32 sets of clocks, 32 sets of waveforms and one PMU channel. It provides fast and accurate testing, with same performance and features as other stand ATE equipment.

#### Sequence Pattern Generator

The Sequence Pattern Generator of the 36010 module provides more than 17 sequence commands including "jump", "match", "loop", "repeat", etc. to control the flow of pattern execution. It equips with 32M sequence command memory, which allows each vector to has its own sequence command to control the flow of pattern execution flexibly. Besides, each Sequence Pattern Generator can support up to 8 Logic Pin Electronics Cards, which means it can support up to 64 I/O channels and performs testing on 8 DUT simultaneously.

#### Logic Pin Electronics Card

In each Logic Pin Electronics Card, it adopts <sup>®</sup> PINF ICs on it to achieve high timing accuracy and flexible waveform output functions.

The per-pin timing generator provides 32 sets of clock containing 6 programmable edges. As for the per-pin waveform generator, it provides each digital I/O channel 32 sets of programmable waveform with the change-one-the-fly feature. In the analog function, the Logic Pin Electronics card has the tri-level driver and comparator with 610uV programmable resolution. It also equips with active load, per-pin PMU and high voltage driver functions. Moreover, the 36010 supports scan pattern function for scan test.

#### Proprietary Software, CRISP

In addition to support the LabView and LabWindows environments, <sup>®</sup> also provides the proprietary software option, CRISP. To cover the various requirements for the IC debugging, CRISP contains lots of software modules. Running on the Microsoft Windows XP<sup>®</sup> operation system and using C++ as the test program language, CRISP provides users the flexible, easy-to-use and fast-runtime GUI software to meet the various demands. The project IDE tool makes it easy to create the test program quickly. In the test program debugging stage, CRISP provides the suite of debugging software tools for user, which includes Plan Debugger, Datalog, Waveform, Scope, SHMOO, Pin Margin, Wafer Map, Summary, Histogram, STDF, Test Condition Monitor, Pattern Editor, and so on.



## SPECIFICATIONS

<b>Model</b>	<b>36010</b>
Test Rate	50/100MHz
Channels Per Board	8 (Scalable to 64 channels)
Vector Depth	32M
Sequence Control Memory	32M
Number of Sequence Control Command	17
Parallel test capability	8
<b>Timing Generator Per Pin</b>	
No. of Edges	6 edges / pin (2 Driver, 2 Driver & I/O, 2 Strobe)
No. of Timing Sets	32 sets / pin
Rate / Edge Setting Resolution	125ps / 62.5ps
Rate Setting Range	20nS → 1mS
<b>Waveform Generator Per Pin</b>	
No. of Waveform Sets	32 sets / pin
<b>Driver</b>	
VIL/VIH Range	-1.5V~+5.9V / -1.4V~+6V
VIL/VIH Accuracy	± 5mV@VIH ≥ VIL+200mV
Output Current (Static/Dynamic)	± 50mV/ ± 100mA
Output Impedance	50 ± 5 Ω
<b>Comparator</b>	
VOL/VOH Range	-1.5V ~ +6V
VOL/VOH Accuracy	± 15mV
<b>Programmable Load</b>	
IOL/IOH Range	± 12mA
IOL/IOH Accuracy	± 25uA
VREF Setting Range	-1.5V ~ +6V
VREF Accuracy	± 50mV

<b>High Voltage Driver</b>	
HV Channel	1 HV channels / board
VIL/VIH Range	0V ~ +13.5V
VIL/VIH Accuracy	± 20mV
VIL/VIH Output Current	± 60mA
<b>Scan Chain</b>	
Chain number / LPC	1/2/4
Size per chain	256M/128M/64M
<b>PPMU</b>	
Channel Number	1 channel / 1 pin
Voltage Force Range	-1.5V ~ +6V
Current Measured Range	32mA/2mA/200μA/20μA/2μA
Current Forced Range	32mA/2mA/200μA/20μA/2μA
Voltage Measured Range	-1.5V ~ +6V
<b>Power and Dimensions</b>	
Power Consumption	25W per Slot
Size	PXI 3U Standard Board (Extendable)
Cooling System	Standard PXI Chassis Fan (Forced Air Cooling)

All specifications are subject to change without notice.

# FOUR CHANNELS DUT POWER SUPPLY MODEL 36020

The 36020 is a four channels programmable DUT power supply in a single-slot 3U PXI module. Each 36020 features 4 channels with the ability to source voltage and measure current. There are two selectable voltage ranges, +5V/-2V and +10V/-2V, with 16-bit resolution for programming the voltage output. In order to provide better accuracy, 36020 provides six selectable current ranges including  $\pm 5\mu\text{A}$ ,  $\pm 25\mu\text{A}$ ,  $\pm 250\mu\text{A}$ ,  $\pm 2.5\text{mA}$ ,  $\pm 25\text{mA}$  and  $\pm 250\text{mA}$  with 18-bit resolution for the current measurement functionality. Moreover, the board-to-board isolation design makes it possible to source the larger voltage than 10V by the series connection with multiple 36020 modules. The versatile supply rails and high accuracy make 36020 an excellent general-purpose, four channels power supply for design validation and manufacturing test application. Especially, the extraordinary accuracy in the small current measurement makes the 36020 very suitable for semiconductor IC test.

## Power Supply with Precision Source and Measurement Capability

The 36020 uses a combination of switching and linear regulation to provide the excellent voltage source and accuracy. It has the ability to source voltage from each of its four outputs. It can be programmed in  $113\mu\text{V}$  steps

on the +5V/-2V range and  $189\mu\text{V}$  steps on the +10V/-2V channels. As a current measure unit, it can measure in minimum  $47.6\text{pA}$  resolution on each channel in the  $\pm 5\mu\text{A}$  current range. You can use this impressive level of current resolution in many power supply applications.

## Proprietary Software, CRISP

In addition to support the LabView and LabWindows environment, Chroma® provides the front panel tool of the 36020 for users to quickly troubleshoot or debug. Users can monitor or refer the setting of the 36020 through this front panel tool. Besides, Chroma® also provides the proprietary software option, CRISP, for the 36020 to meet the demands of users for various purposes. Based on Microsoft Windows XP® operation system and C++ programming language, CRISP provides the powerful, easy-to-use, intuitive, and fast-runtime GUI tools for users. For the test debugging and data analyzing purposes, CRISP provides users the abundant software modules for the 36020, including Datalog, SHMOO, Summary, Histogram, STDF and Test Condition Monitor.

## MODEL 36020

### KEY FEATURES

- 4 Channels in standard PXI 3U/ PXIe-hybrid compatible bus type
- +5V/-2V and +10V/-2V force ranges
- 16-bit voltage force resolution
- 18-bit current measurement resolution
- 6 selectable ranges from  $5\mu\text{A}$  to  $250\text{mA}$  for current measurement
- Programmable current clamp function
- Ganged function available for larger current
- Board-to-board isolation
- Windows XP/7 operating system
- Support LabView and LabWindows
- Proprietary software tools for data analysis

### APPLICATIONS

- Logic and mixed signal validation and test
- Consumer IC and electronics test
- DUT Power Supply



## SPECIFICATIONS

Model	36020	
Input	PXI Internal Power	
Channel Number	4	
Voltage Source		
Range	VR1: +10v/-2v VR2: +5v/-2v	
Resolution	16bits	
Accuracy	$\pm 0.05\% + 1\text{mV}$	
Noise	3mVrms	
Current Measurement		
Range	$\pm 5\mu\text{A}, \pm 25\mu\text{A}, \pm 250\mu\text{A}, \pm 2.5\text{mA}, \pm 25\text{mA}, \pm 250\text{mA}$	
Resolution	18bits	
Accuracy	250mA	$\pm 0.05\% + 100\mu\text{A}$
	25mA	$\pm 0.05\% + 10\mu\text{A}$
	2.5mA	$\pm 0.05\% + 1\mu\text{A}$
	250 $\mu\text{A}$	$\pm 0.05\% + 100\text{nA} + 1\text{nA/V}$
	25 $\mu\text{A}$	$\pm 0.105\% + 20\text{nA} + 1\text{nA/V}$
	5 $\mu\text{A}$ range	$\pm 0.05\% + 10\text{nA} + 1\text{nA/V}$
Slew Rate	0.7V / 1 $\mu\text{s}$	
Load Regulation	2mV	
Load Transient		
Time Response	100 $\mu\text{s}$	
Voltage Response	50mv	
Overshoot/Undershoot	<3%	
Clamp Flag Response	100 $\mu\text{s}$	
Clamp Resolution	10bits	
Protection Function / Alarm Flag	Short current limit Clamp alarm flag	
Max Stable Load Capacitance	100 $\mu\text{F}$	

All specifications are subject to change without notice.

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